Division Code;	VCT	Department Code:	VASD	Effective Term <u>Fall</u> Org #: <u>282/</u>
Don't publish:	College Catalog	∑Time Schedule	⊠Web Page	
⊠New course	syllabus review/Assessmen	<u>_</u>	Reactivation of inactive Inactivation (Submit this	course s page only.)
Change informa	ation: Note all changes t	that are being made. For	m applies only to chang	res noted.
required. Course disci *Must subm Course title (Course desci Course object	pline code & number (was nit inactivation form for pr (was	revious course.	Distribution of contact h lecture: lab	
	urse of course change. A	mach course assessment	report for existing cours	ses that are being changed.
	1 1			
Approvals Departr Department I	ment and divisional signatu Review by Chairperson	ures indicate that all departn		
Department I	Review by Chairperson OM WELCH	Ires indicate that all departs New resources neede Signature	d All relevant dep	partments consulted
Department I	Review by Chairperson	New resources neede	d All relevant dep	partments consulted Date: Z-Z
Print: Print: Division Revi	Review by Chairperson Welch Faculty/Preparer Department Chair lew by Dean	New resources neede	d All relevant dep	partments consulted Date: Z-Z
Print: Print: Division Revi	Review by Chairperson Welch Faculty/Preparer Department Chair iew by Dean r conditional approval ion Yes No	New resources neede Signature Signature Ohrech	d	Date:
Print: Division Revi Request for Recommendati	Review by Chairperson Welch Faculty/Preparer Department Chair iew by Dean r conditional approval ion Yes No I Committee Review	New resources neede Signature Signature	d	Date: Date:
Print: Print: Division Revi Request for Recommendati	Review by Chairperson Welch Faculty/Preparer Department Chair lew by Dean r conditional approval ion Yes No I committee Review ion Yes No	New resources neede Signature Signature Ohrech	d	Date: 2-2
Print: Print: Division Revi Request for Recommendati Curriculum C Recommendati Tabled	Review by Chairperson Welch Faculty/Preparer Department Chair lew by Dean r conditional approval ion Yes No I committee Review ion Yes No	New resources neede Signature Signature Dean's/Administrator's Signature Curriculum Committee Characterists	d	Date: 2-2-0
Print: Print: Division Revi Request for Recommendati Curriculum C Recommendati Tabled Vice President	Review by Chairperson Welch Faculty/Preparer Department Chair lew by Dean r conditional approval ion Yes No Iommittee Review ion Yes No	Signature Signature Signature Signature Curriculum Committee Challed Committee C	d	Date:

Office of Curriculum & Assessment

Approved by Assessment Committee 10/06

*Complete ALL sections v	which apply to the course, even	if changes are not bais			
Course:	Course title:	in changes are not bein	ig made.		
EWA170	Semiconductors				
Credit hours: 2	Contact hours per semester:	Are lectures, labs, or	Grading options:		
If variable credit, give range:	Student Instructor	clinicals offered as separate sections?	P/NP (limited to clinical & practical)		
tocredits	Lecture: 30 30 Lab:	Yes - lectures, labs, or clinicals are offered in separate sections No - lectures, labs, or clinicals are offered in the same section	□S/U (for courses numbered below 100) □Letter grades		
Prerequisites. Select one:					
⊠College-level Reading & Writin	Reduced Reading/ (Add information at Lev	•	No Basic Skills Prerequisite (College-level Reading and Writing is <u>not</u> required.)		
In addition to Basic Skills in R	eading/Writing:				
Level I (enforced in Banner) Course	Grade Test	Min. Score Concurr			
		Min. Score Concurre Enrollme Can be taken to	ent Must be enrolled in this class		
☐ and ☐ or					
and or					
and or					
Level II (enforced by instructor or	n first day of class) Course	Grade Test	Min. Score		
☐ and ☐ or					
Enrollment restrictions (In addit	tion to prerequisites, if applicable.)				
□and □or Consent required	□and ⊠or Admission Program: <u>I</u> I	to program required BEW 252 Apprenticeship	□and □or Other (please specify):		
Please send syllabus for trans Conditionally approved courses Insert course number and title yo					
☐ E.M.U. as			as		
U of M as			as		
as		 	as		
		<u> </u>	as		

Course	Course title				
EWA170	Semiconductors				
Course description State the purpose and content of the course. Please limit to 500 characters.	Students are introduced to the basic theory of operation of semiconductor devices. The basics manufacture and construction of P-type and N-type semiconductor materials and the theory of the PN junction are discussed and then expanded upon with the introduction multilayer devices. This course is taught at the IBEW local training center and is only open to apprentices accepted into a program.				
Course outcomes	Outcomes	Assessment			
List skills and knowledge students will have after	(applicable in all sections)	Methods for determining course effectiveness			
Assessment method Indicate how student achievement in each outcome will be assessed to determine student achievement for purposes of course improvement.	After successful completion of this course, the student will be able to: 1. Explain the composition of P-type and N-type semiconductor materials 2. Operate a P-N junction 3. Identify and explain various semiconductor devices 4. Discuss basics of amplifier circuits	This course is assessed externally by the local's Joint Apprenticeship Training Committee (JATC), consisting of NECA representatives (industry) and IBEW members. The local receives feedback on needed technical updates and apprentice skill performance.			
Course Objectives Indicate the objectives that support the course outcomes given above. Course Evaluations Indicate how instructors will determine the degree to which each objective is met for each student.	Objectives (applicable in all sections) Objectives and methods of evaluation follow the curriculum set out by the National Joint Apprentice Training Committee (NJATC).	Evaluation Methods for determining level of student performance of objectives			

List all new resources needed for course, including library materials.

All resources for the pro gram are in place at the Local 252 Training Center.

Student Materials:

List examples of types	All books and supplies provided through the IBEW Local 252 Training Center.	Estimated costs
Texts	0	Estimated costs
Supplemental reading		\$ O
Supplies		
Uniforms		
Equipment		
Tools		
Software		

Level I classroom				
Permanent screen & overhead projector Level II classroom Level II classroom Level III	☐ Off-Campus Sites ☐ Testing Center			
Level II classroom Level II equipment plus TV/VCR Level III classroom Level III classroom Level III equipment plus data projector, computer, faculty workstation Assessment plan: Course assessed (list from Page 3) Contractors (employer) provide paper feedback forms for apprentice skill performance reviews. Description of Page 3 Contractors (employer) provide paper feedback forms for apprentice skill performance reviews. Discuss basics of amplifier circuits JATC contractor members provide specifications detailing technical updates. JATC contractor members provide specifications detailing technical updates. Level III class projector/computer Data projector/computer Data projector/computer Data projector/computer Course section(s)/other (semester & year) Fall 2011 and every three years thereafter Fall 2011 and every three years thereafter Fall 2011 and every three years thereafter Scoring and analysis of assessment: Indicate how the above assessment(s) will be scored and evaluated (e.g. departmentally developed rubric, external evaluation, other). Attach the rubric/scoring guide. Apprentice feedback forms filled out by the employing contractor.				
Level II classroom Level III classroom Level III classroom Level III equipment plus data projector, computer, faculty workstation Data projector/computer				
Assessment plan: Learning outcomes to be assessed (list from Page 3) 1. Explain the composition of P-type and N-type semiconductor materials 2. Operate a P-N junction 3. Identify and explain various semiconductor devices 4. Discuss basics of amplifier circuits JATC contractor members provide specifications detailing technical updates. JATC contractor members provide specifications detailing technical updates. Scoring and analysis of assessment: 1. Indicate how the above assessment(s) will be scored and evaluated (e.g. departmentally developed rubric, external evaluation, other). Attach the rubric/scoring guide.				
Assessment plan: Learning outcomes to be assessed (list from Page 3) 1. Explain the composition of P-type and N-type semiconductor materials 2. Operate a P-N junction 3. Identify and explain various semiconductor devices 4. Discuss basics of amplifier circuits JATC contractor members provide specifications detailing technical updates. JATC contractor members provide specifications detailing technical updates. Jata Contractor members provide specifications detailing technical updates. Scoring and analysis of assessment: I. Indicate how the above assessment(s) will be scored and evaluated (e.g. departmentally developed rubric, external evaluation, other). Attach the rubric/scoring guide. Apprentice feedback forms filled out by the employing contractor.	Data projector/computer			
Learning outcomes to be assessed (list from Page 3) 1. Explain the composition of P-type and N-type semiconductor materials 2. Operate a P-N junction 3. Identify and explain various semiconductor devices 4. Discuss basics of amplifier circuits Scoring and analysis of assessment: 1. Indicate how the above assessment(s) will be scored and evaluated (e.g. departmentally developed rubric, extending guide. Assessment tool When assessment will take place (semester & year) Pall 2011 and every three years thereafter Fall 2011 and every three years thereafter All Three years thereafter Scoring and analysis of assessment: Indicate how the above assessment(s) will be scored and evaluated (e.g. departmentally developed rubric, extending guide. Apprentice feedback forms filled out by the employing contractor.	<u>-</u>			
Learning outcomes to be assessed (list from Page 3) 1. Explain the composition of P-type and N-type semiconductor materials 2. Operate a P-N junction 3. Identify and explain various semiconductor devices 4. Discuss basics of amplifier circuits Scoring and analysis of assessment: 1. Indicate how the above assessment(s) will be scored and evaluated (e.g. departmentally developed rubric, extending guide. Assessment tool when assessment will take place (semester & year) population Fall 2011 and every three years thereafter Fall 2012 and year year year year year year year year				
1. Explain the composition of P- type and N-type semiconductor materials 2. Operate a P-N junction 3. Identify and explain various semiconductor devices 4. Discuss basics of amplifier circuits Scoring and analysis of assessment: 1. Indicate how the above assessment(s) will be scored and evaluated (e.g. departmentally developed rubric, external explain the composition of P- type and N-type semiconductor materials skill performance reviews. JATC contractor members provide specifications detailing technical updates. Scoring and analysis of assessment: 1. Indicate how the above assessment(s) will be scored and evaluated (e.g. departmentally developed rubric, external explanation). Attach the rubric/scoring guide. Apprentice feedback forms filled out by the employing contractor.	Number students to			
type and N-type semiconductor materials 2. Operate a P-N junction 3. Identify and explain various semiconductor devices 4. Discuss basics of amplifier circuits Scoring and analysis of assessment: 1. Indicate how the above assessment(s) will be scored and evaluated (e.g. departmentally developed rubric, extending guide. Contractors (employer) provide paper feedback forms for apprentice skill performance reviews. Fall 2011 and every three years thereafter JATC contractor members provide specifications detailing technical updates. Scoring and analysis of assessment: 1. Indicate how the above assessment(s) will be scored and evaluated (e.g. departmentally developed rubric, extending guide. Apprentice feedback forms filled out by the employing contractor.	be assessed			
semiconductor devices 4. Discuss basics of amplifier circuits Scoring and analysis of assessment: 1. Indicate how the above assessment(s) will be scored and evaluated (e.g. departmentally developed rubric, ext evaluation, other). Attach the rubric/scoring guide. Apprentice feedback forms filled out by the employing contractor.	All			
 Indicate how the above assessment(s) will be scored and evaluated (e.g. departmentally developed rubric, ext evaluation, other). Attach the rubric/scoring guide. Apprentice feedback forms filled out by the employing contractor. 				
Apprentice feedback forms filled out by the employing contractor.				
	ernal			
2. Indianalla, 1.1.6				
2. Indicate the standard of success to be used for this assessment.				
The standard of success is set by the local JATC.				

Approved by Assessment Committee 10/06

Indicate who will score and analyze the data (data must be blind-scored).

Explain the process for using assessment data to improve the course.

The data is analyzed by the JATC as a committee.

appropriate instructor staff to make needed changes.

Results are initially shared with the training coordinator for the local. The training coordinator then works with